

PATENT

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April 23, 2004

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ATTN: Special Program Examiner Steve MarcusFAX # (703) 305-3579
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FROM: Edward W. Bulchis, Esq.

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Applicant : Scott E. Moore

Attorney Docket No.: 500199.05

Serial No. : 09/929,137

Group Art Unit : 3723

Filed : August 13, 2001

Examiner : Maurina T. Rachuba

Title : METHOD AND APPARATUS FOR PLANARIZING A MICROELECTRONIC
SUBSTRATE WITH A TILTED PLANARIZING SURFACE

Date Sent : April 23, 2004

I hereby certify that the attached is a true copy of a Response to Restriction Requirement that was mailed to the USPTO on the date of the certificate of mailing, October 6, 2003, addressed to Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450. Also attached is a copy of the date-stamped return receipt postcard providing proof of timely receipt of the response by the U.S. Patent and Trademark Office on October 9, 2003.

April 23, 2004
DateDenise Sheridan
Denise Sheridan**Attachments:****# of Pages**

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I hereby certify that on the date specified below, this correspondence is being deposited with the United States Postal Service as first-class mail in an envelope addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

October 6, 2003

Date

Denise Sheridan

Denise Sheridan

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Scott E. Moore

Attorney Docket No.: 500199.05 (29243/US/4)

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Title : METHOD AND APPARATUS FOR PLANARIZING A MICROELECTRONIC
SUBSTRATE WITH A TILTED PLANARIZING SURFACERESPONSE TO RESTRICTION REQUIREMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

In response to the Restriction Requirement dated September 17, 2003, applicant hereby elects Species 1, claims 1-16 and 44-48, for examination at this time.

Prompt examination of the elected claims is respectfully requested, and a Notice of Allowance is earnestly solicited.

Respectfully submitted,
DORSEY & WHITNEY LLP

Steven H. Arterberry
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Enclosure:

Postcard

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SENT: October 6, 2003
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Response to Restriction Requirement; in re: Scott E. Moore, USAN 09/929,137, filed August 13, 2001, for METHOD AND APPARATUS FOR PLANARIZING A MICROELECTRONIC SUBSTRATE WITH A TILTED PLANARIZING SURFACE.

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